

**IN THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

Claims 1-16 (Canceled)

17. (New) An active matrix display device comprising a driving circuit including:  
first to n-th register circuits connected to a clock pulse input terminal; and  
first to n-th digital data latch circuits connected to a digital data input terminal,  
corresponding one of the first to n-th register circuits, and corresponding one of first to n-  
th digital data output terminals;

wherein:

a k-th circuit of the first to n-th register circuits is connected to a (k+1)-th circuit  
of the first to n-th register circuits, and a (k-1)-th circuit of the first to n-th digital data  
latch circuits;

the letters n and k denote a natural number; and

the k is smaller than or equal to n.

18. (New) An active matrix display device according to claim 17, wherein a  
transistor included in the driving circuit is a TFT.

19. (New) An electric equipment comprising the active matrix display device  
according to claim 17, selected from the group consisting of a projector, rear projector,

front projector, goggle type display, mobile computer, cellular phone, notebook personal computer, car navigation, video camera, DVD player, and game machine.

20. (New) An active matrix display device comprising a driving circuit including:

first to n-th register circuits having:

a clocked inverter circuit connected to a clock pulse input terminal; and

an inverter circuit connected to the clocked inverter circuit;

first to n-th digital data latch circuits having:

a first transistor connected to a digital data input terminal;

a second transistor connected to the first transistor, and the inverter circuit

of corresponding one of the first to n-th register circuit;

a resetting means;

a digital data holding circuit connected to the second transistor, the  
resetting means, and corresponding one of first to n-th digital data output  
terminals;

wherein:

the inverter circuit of k-th register circuit is connected to the clocked inverter  
circuit of (k+1)-th register circuit, and the first transistor of (k-1)-th digital data latch  
circuit;

the letters n and k denote a natural number; and

the k is smaller than or equal to n.

21. (New) An active matrix display device according to claim 20, wherein a transistor included in the driving circuit is a TFT.

22. (New) An active matrix display device according to claim 20, wherein the digital data holding circuit has two inverter circuits.

23. (New) An active matrix display device according to claim 20, wherein the digital data holding circuit has a capacitance.

24. (New) An active matrix display device according to claim 20,  
wherein:  
the resetting means has a third transistor connected to a reset signal input terminal; and  
polarity of the third transistor is reversal from that of the first and second transistors.

25. (New) An active matrix display device according to claim 20, wherein the resetting means has a resistance.

26. (New) An electric equipment comprising the active matrix display device according to claim 20, selected from the group consisting of a projector, rear projector, front projector, goggle type display, mobile computer, cellular phone, notebook personal computer, car navigation, video camera, DVD player, and game machine.

27. (New) An active matrix display device comprising a driving circuit including:

first to n-th register circuits connected to a clock pulse input terminal;

first to n-th switching circuits connected to L/R direction selecting signal input terminal, and corresponding one of the first to n-th register circuits; and

first to n-th digital data latch circuits connected to a digital data input terminal, corresponding one of the first to n-th switching circuits, and corresponding one of first to n-th digital data output terminals;

wherein:

a k-th circuit of the first to n-th switching circuits is connected to a (k+2)-th circuit of the first to n-th switching circuits, a (k+1)-th circuit of the first to n-th register circuits, and a (k-1)-th circuit of the first to n-th digital data latch circuits;

the letters n and k denote a natural number; and

the k is smaller than or equal to n.

28. (New) An active matrix display device according to claim 27, wherein a transistor included in the driving circuit is a TFT.

29. (New) An electric equipment comprising the active matrix display device according to claim 27, selected from the group consisting of a projector, rear projector, front projector, goggle type display, mobile computer, cellular phone, notebook personal computer, car navigation, video camera, DVD payer, and game machine.

30. (New) An active matrix display device comprising a driving circuit including:

first to n-th register circuits having:

a clocked inverter circuit connected to a clock pulse input terminal; and

an inverter circuit connected to the clocked inverter circuit;

first to n-th switching circuits connected to L/R direction selecting signal input terminal, and the inverter circuit of corresponding one of the first to n-th register circuits;

first to n-th digital data latch circuits having:

a first transistor connected to a digital data input terminal;

a second transistor connected to the first transistor, and corresponding one of the first to n-th switching circuit;

a resetting means;

a digital data holding circuit connected to the second transistor, to the resetting means, and corresponding one of first to n-th digital data output terminals;

wherein:

a k-th circuit of the first to n-th switching circuit is connected to a (k+2)-th circuit of the first to n-th switching circuits, the clocked inverter circuit of (k+1)-th register circuit, and the first transistor of (k-1)-th digital data latch circuit;

the letters n and k denote a natural number; and

the k is smaller than or equal to n.

31. (New) An active matrix display device according to claim 30, wherein the digital data holding circuit has two inverter circuits.

32. (New) An active matrix display device according to claim 30, wherein the digital data holding circuit has a capacitance.

33. (New) An active matrix display device according to claim 30, wherein:  
the resetting means has a third transistor connected to a reset signal input terminal; and

polarity of the third transistor is reversal from that of the first and second transistors.

34. (New) An active matrix display device according to claim 30, wherein the resetting means has a resistance.

35. (New) An electric equipment comprising the active matrix display device according to claim 30, selected from the group consisting of a projector, rear projector, front projector, goggle type display, mobile computer, cellular phone, notebook personal computer, car navigation, video camera, DVD player, and game machine.

36. (New) An active matrix display device comprising a driving circuit including:

first to n-th register circuits connected to a clock pulse input terminal; and

first to n-th digital data latch circuits connected to a digital data input terminal, corresponding one of the first to n-th register circuits, and corresponding one of first to n-th digital data output terminals;

wherein:

a k-th circuit of the first to n-th register circuits is connected to a (k+1)-th circuit of the first to n-th register circuits, and a (k-1)-th circuit of the first to n-th digital data latch circuits;

the letters n and k denote a natural number; and

the k is smaller than or equal to n.

37. (New) An active matrix display device according to claim 36, wherein a transistor included in the driving circuit is a TFT.

38. (New) An electric equipment comprising the active matrix display device according to claim 36, selected from the group consisting of a projector, rear projector, front projector, goggle type display, mobile computer, cellular phone, notebook personal computer, car navigation, video camera, DVD player, and game machine.

39. (New) An active matrix display device comprising a driving circuit including:  
first to n-th register circuits connected to a clock pulse input terminal;  
first to n-th switching circuits connected to L/R direction selecting signal input terminal, and corresponding one of the first to n-th register circuits; and

first to n-th digital data latch circuits connected to a digital data input terminal, corresponding one of the first to n-th switching circuits, and corresponding one of first to n-th digital data output terminals;

wherein:

a k-th circuit of the first to n-th switching circuits is connected to a (k+2)-th circuit of the first to n-th switching circuits, a (k+1)-th circuit of the first to n-th register circuits, and a (k-1)-th circuit of the first to n-th digital data latch circuits;

the letters n and k denote a natural number; and

the k is smaller than or equal to n.

40. (New) An active matrix display device according to claim 39, wherein a transistor included in the driving circuit is a TFT.

41. (New) An active matrix display device according to claim 39, wherein a transistor included in the driving circuit is a TFT.

42. (New) An electric equipment comprising the active matrix display device according to claim 39, selected from the group consisting of a projector, rear projector, front projector, goggle type display, mobile computer, cellular phone, notebook personal computer, car navigation, video camera, DVD player, and game machine.

43. (New) An active matrix display device comprising a driving circuit including:

first to n-th register circuits having:

a clocked inverter circuit connected to a clock pulse input terminal; and  
an inverter circuit connected to the clocked inverter circuit;  
first to n-th switching circuits connected to L/R direction selecting signal input  
terminal, and the inverter circuit of corresponding one of the first to n-th register circuits;  
first to n-th digital data latch circuits having:  
a first transistor connected to a digital data input terminal;  
a second transistor connected to the first transistor, and corresponding one  
of the first to n-th switching circuit;  
a resetting means;  
a digital data holding circuit connected to the second transistor, to the  
resetting means, and corresponding one of first to n-th digital data output  
terminals;  
wherein:  
a k-th circuit of the first to n-th switching circuit is connected to a (k+2)-th circuit  
of the first to n-th switching circuits, the clocked inverter circuit of (k+1)-th register  
circuit, and the first transistor of (k-1)-th digital data latch circuit;  
the letters n and k denote a natural number; and  
the k is smaller than or equal to n.

44. (New) An active matrix display device according to claim 43, wherein a  
transistor included in the driving circuit is a TFT.

45. (New) An active matrix display device according to claim 43, wherein the digital data holding circuit has two inverter circuits.

46. (New) An active matrix display device according to claim 43, wherein the digital data holding circuit has a capacitance.

47. (New) An active matrix display device according to claim 43, wherein:  
the resetting means has a third transistor connected to a reset signal input terminal; and  
polarity of the third transistor is reversal from that of the first and second transistors.

48. (New) An active matrix display device according to claim 43, wherein the resetting means has a resistance.

49. (New) An electric equipment comprising the active matrix display device according to claim 43, selected from the group consisting of a projector, rear projector, front projector, goggle type display, mobile computer, cellular phone, notebook personal computer, car navigation, video camera, DVD player, and game machine.

If any fee is due for this amendment, please charge our deposit account 50/1039.

Favorable consideration is earnestly solicited.

Respectfully submitted,

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